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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TONOMURA, Motonobu et al.

Intl. Appln. No.: PCT/JP02/06166

Intl. Filing Date: 20 June 2002

For: CODE CALCULATING DEVICE

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA   22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

     Pursuant to 37 C.F.R. § 1.56, and without any assertion

as to materiality or prior art effect, the documents listed

on the attached Form PTO-1449 are hereby cited.

     Documents AH-AJ and AR on the attached List were cited

in the International Search Report (copy attached). The

Office should have received copies of the these documents

from the PCT International Authority (M.P.E.P. 1893.03(g)).

So copies are not being submitted herewith in order to avoid

duplication. If the Office did not receive the copies from

the PCT International Authority, the Examiner is requested

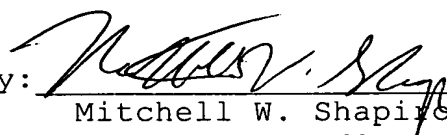to telephone the undersigned so that copies may be provided.

Documents AH and AO-AQ are cited in the specification,

on pages 2 and 4, and their relevance is indicated therein.

Copies of Documents AO-AQ are submitted herewith.

Respectfully submitted,

MWS:sjk

By: _____
        Mitchell W. Shapiro
        Reg. No. 31,568

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December 20, 2004

FORM PTO-●●9 INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT	Atty. Docket No. XA-10241	Appln. No. 10/518481

	Applicant TONOMOURA, Motonobu et al.
	Filing Date Herewith	Group Art Unit

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub- class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Sub- class	Translation
	AH	WO 91/20028	12/26/91	WIPO			
	AI	2001-56640	02/27/01	Japan			
	AJ	7-50595	02/21/95	Japan			
	AK						
	AL						
	AM						
	AN						

OTHER (including author, title, date, pertinent pages, etc.)

	AO	Ramabadran, T. V. et al., "A Tutorial on CRC Computations," IEEE Micro, Vol. 8, No. 4, August 1988, pp. 62-75.
	AP	Moon, S. et al. "Fast VLSI Arithmetic Algorithms for High-Security Elliptic Curve Cryptographic Applications," IEEE Transaction Consumer Electronics, Vol. 47, No. 3, August 2001, pp. 700-708.
	AQ	Mastrovito, E., "VLSI Designs for Multiplication over Finite Fields $GF(2^m)$," Proc. Sixth Int'l Conf. "Applied Alegbra, Algebraic Algorithms, and Error-Connecting Codes (AAECC-6)," July 1988, pp. 297-309.
	AR	Shiba, K. et al., "$GF(2^m)$ Ensan oyobi Seisu Ensan o Shori Kano na Hybrid Coprocessor no Teian," 1999nen Ango to Joho Security Symposium Yokoshu, Volume II of II, January 26, 1999, pp. 819-824.

Examiner	Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.